

Electronic Acknowledgement Receipt

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| EFS ID: | 5759496 |
| Application Number: | 10829495 |
| International Application Number: | |
| Confirmation Number: | 6926 |
| Title of Invention: | Method and system for fail-safe renaming of logical circuit identifiers for rerouted logical circuits in a data network |
| First Named Inventor/Applicant Name: | William Taylor |
| Customer Number: | 83417 |
| Filer: | Felipe Hernandez/Kathryn Bartelt |
| Filer Authorized By: | Felipe Hernandez |
| Attorney Docket Number: | 20103/030290 |
| Receipt Date: | 23-JUL-2009 |
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| Time Stamp: | 17:15:21 |
| Application Type: | Utility under 35 USC 111(a) |

Payment information:

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|--|---------------------------|
| Submitted with Payment | yes |
| Payment Type | Electronic Funds Transfer |
| Payment was successfully received in RAM | \$ 940 |
| RAM confirmation Number | 3536 |
| Deposit Account | |
| Authorized User | |

File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
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|--|--|-------------------------------|---|----|----|
| 1 | Request for Continued Examination (RCE) | 030290_RCE_72309.pdf | 78687 9e57616ac64c9d40192adbdb0cac76369ffe5adb | no | 3 |
| Warnings: | | | | | |
| This is not a USPTO supplied RCE SB30 form. | | | | | |
| Information: | | | | | |
| 2 | Amendment After Final | 030290_FOA_Response_72309.pdf | 134279 00b368514053480d0088ee8546dc6781d041a7a | no | 10 |
| Warnings: | | | | | |
| Information: | | | | | |
| 3 | Information Disclosure Statement (IDS) Filed (SB/08) | 030290_IDS_72309.pdf | 87762 be5e1bc13e9484ccadf32ef2a462e0479d7a6a4c | no | 4 |
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| 4 | Fee Worksheet (PTO-875) | fee-info.pdf | 31904 4a3358f7d6de20e5dc1104f62178239702d9e711 | no | 2 |
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| Total Files Size (in bytes): | | | 332632 | | |
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